Application Data Sheet

Application Information

Application number::

Filing Date::

Application Type:: Regular Subject Matter:: Utility

Title :: METHOD FOR

MANUFACTURING THIN

FILM INTEGRATED

CIRCUIT, AND ELEMENT

SUBSTRATE

Attorney Docket Number:: 0756-7782

Total Drawing Sheets:: 30
Small Entity?:: No

Applicant Information

Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan
Given Name:: Shunpei

Middle Name::

Family Name:: YAMAZAKI

Name Suffix::

City of Residence:: Setagaya

State or Province of Residence:: Tokyo
Country of Residence:: Japan

Street of mailing address:: c/o Semiconductor Energy

Laboratory Co., Ltd.

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398, Hase

City of mailing address:: Atsugi-shi

State or Province of mailing address:: Kanagawa-ken

Country of mailing address:: Japan

Postal or Zip Code of mailing address:: 243-0036

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Japan

Given Name:: Koji

Middle Name::

Family Name:: DAIRIKI

Name Suffix::

City of Residence:: Isehara

State or Province of Residence:: Kanagawa

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398, Hase

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Correspondence Information

Correspondence Customer Number :: 31780

E-Mail address:: erobinson@riplo.com

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Representative Information

Representative Customer Number:: 31780

Domestic Priority Information

Application ::	Continuity Type::	Parent Application::	Parent Filing Date::
This application	National Stage	PCT/JP2005/001541	01/27/2005

Foreign Priority Information

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	2004-031064	02/06/2004	Yes

Assignee Information

Assignee name:: Semiconductor Energy

Laboratory Co., Ltd.

Street of mailing address:: 398 Hase

City of mailing address:: Atsugi-shi

State or Province of mailing address:: Kanagawa-ken

Country of mailing address:: Japan

Postal or Zip Code of mailing address:: 243-0036